

# BEST AVAILABLE COPY

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10007754	FILING DATE 11/15/2001	CLASS 256	SUBCLASS 197	GAH 2816 2812	EXAMINER FAYY MANDALIA
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\*\*APPLICANTS: Hill Darrell; Sadaka Mariam; Abrokwhah Jonathan;

JC 3  
JC 8/05

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/641,002 08/17/2000

LATT N 438

281L

\*\*FOREIGN APPLICATIONS VERIFIED:

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	SC10776P D01
Verified and Acknowledged Examiner's initials			
TITLE : Method of manufacturing a semiconductor component and semiconductor component thereof U.S. DEPT. OF COMM. PAT. & TMDL REG. (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Fig.s Drwg.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
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